

FIG. 1

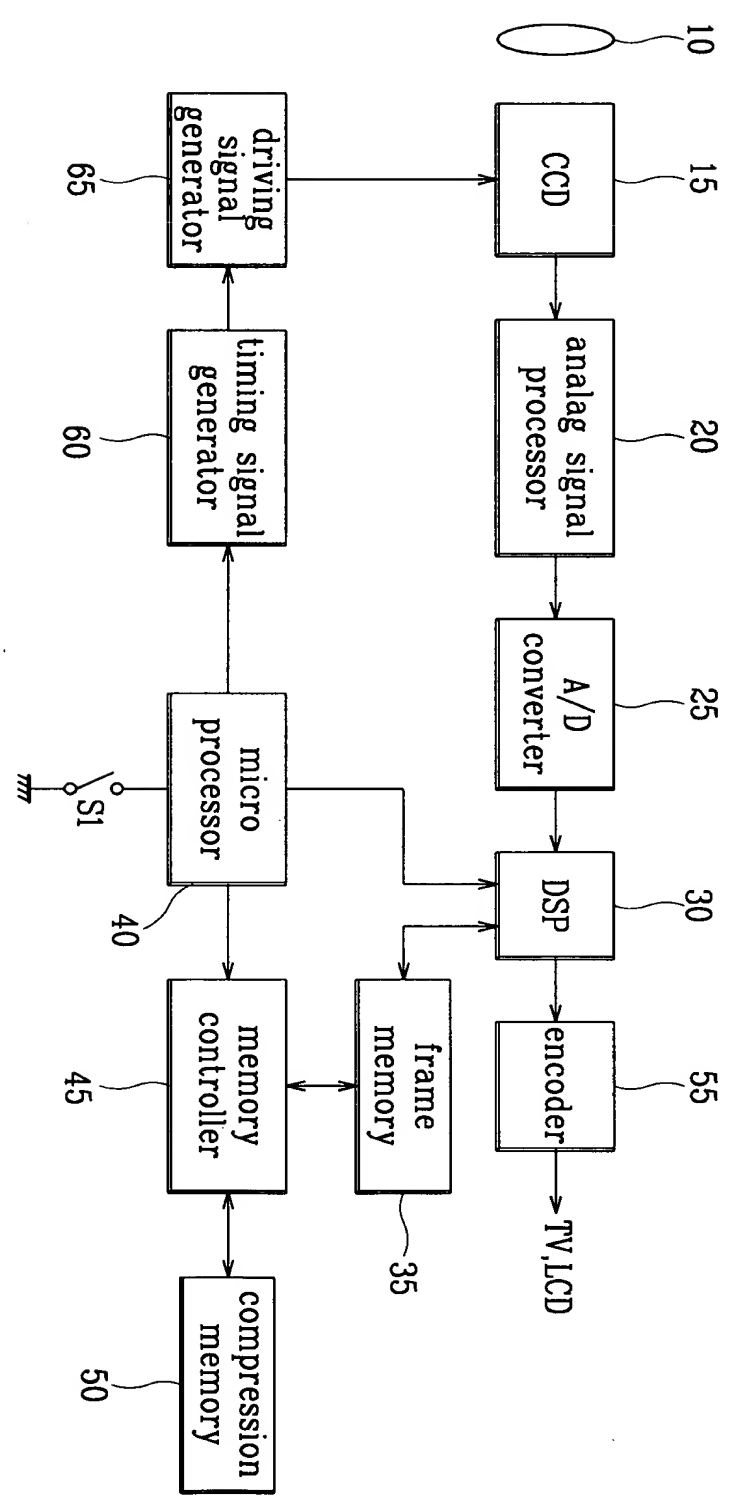


FIG. 1 is a block diagram of a video processing system. The system includes a lens (10) connected to a CCD (15), which feeds into an analog signal processor (20). The processor (20) connects to an A/D converter (25), which then feeds into a DSP (30). The DSP (30) connects to an encoder (55), which outputs to TV, LCD. A micro processor (40) is connected to the DSP (30) and a frame memory (35). The micro processor (40) also controls a driving signal generator (65) and a timing signal generator (60). The driving signal generator (65) feeds into the CCD (15). The timing signal generator (60) feeds into the micro processor (40). The micro processor (40) is connected to a memory controller (45) via a switch S1. The memory controller (45) is connected to a compression memory (50).

FIG.2

